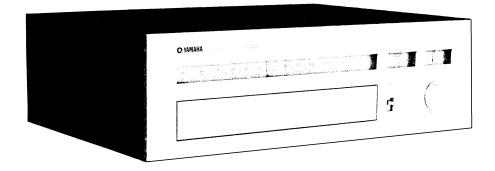


For more Hi-Fi manuals and set-up information please visit www.hifiengine.com

# YAMAHA Hi-Fi STEREO SERVICE MANUAL FM STEREO TUNER MODEL CT-7000



# CONTENTS

SPECIFICATIONS	1
EXTERNAL VIEWS	
FRONT PANEL	2
REAR PANEL (U.S. & CANADIAN MODELS)	2
REAR PANEL (EUROPEAN MODEL)	2
INTERNAL VIEWS	
TOP VIEW	4
BOTTOM VIEW	4
CIRCUIT DESCRIPTION	
PARTIAL DISASSEMBLY	
DIAL MECHANISM	. 17
MEASUREMENT AND ADJUSTMENT	18
PRINTED CIRCUIT BOARD	
BLOCK DIAGRAM	26
OVERALL SCHEMATIC DIAGRAM	. 27
PARTIAL CHANGES MADE ACCORDING TO DESTINATION	. 29
PARTS LIST	31

# **SPECIFICATIONS**

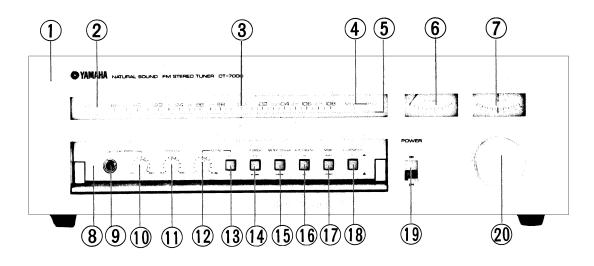
Tuning Range 88-108MHz Sensitivity (mono) Normal: better than 2.5µV IHF Wide: better than 3.2µV DIN (40kHz Dev.; S/N 26dB) Normal: better than 1.6µV Wide: better than 2.5µV Sensitivity (stereo) DIN (40kHz Dev.; S/N 46dB) 50µV Image Frequency Rejection Over 120dB IF Rejection Over 120dB **Spurious Response Rejection** Over 120dB AM Rejection IHF Over 60dB **Capture Ratio** Normal: better than 1.3dB 1mV Wide: better than 1.0dB 10µ∨ Normal: better than 2.0dB Wide: better than 3 0dB 100mV Normal: better than 1.0dB Wide: better than 1.0dB Selectivity IHF (75kHz Dev.) Normal: over 80dB Wide: over 18dB DIN (±300Hz; 40kHz Dev.) ±500kHz Normal: over 65dB Wide: over 10dB Normal: over 90dB Wide: over 35dB Signal/Noise Ratio (mono) IHF (75kHz Dev.) Over 78dB DIN (40kHz Dev.) Over 72dB Signal/Noise Ratio (stereo) IHF (75kHz Dev.) Over 75dB DIN (40kHz Dev.) Over 69dB

**Total Harmonic Distortion** (antenna level: 1mV) Mono IHF (400Hz; 75kHz Dev.) Normal: less than 0.08% Wide: less than 0.08% DIN (1kHz; 40kHz Dev.) Normal: less than 0.08% Wide: less than 0.08% IHF (50-10.000Hz; 75kHz Dev.) Normal: less than 0.3% Wide: less than 0.15% DIN (50-10,000Hz; 40kHz Dev.) Normal: less than 0.3% Wide: less than 0.15% Stereo IHF (400Hz; 75kHz Dev.) Normal: less than 0.08% Wide: less than 0.08% DIN (1kHz; 40kHz Dev.) Normal: less than 0.08% Wide: less than 0.08% IHF (50-10,000Hz; 75kHz Dev.) Normal: less than 0.5% Wide: less than 0.2% DIN (50-10,000Hz; 40kHz Dev.) Normal: less than 0.5% Wide: less than 0.2% **Stereo Separation** IHF (400Hz; 75kHz Dev.) Normal & wide: 50dB DIN (1kHz; 40kHz Dev.) Normal & wide: 50dB IHF (50-10,000Hz; 75kHz Dev.) Normal: 35dB Wide: 40dB DIN (50-10,000Hz; 40kHz Dev.) Normal: 35dB Wide: 40dB Frequency Response 50-10,000Hz ±0.3dB 30-15,000Hz +0.5dB, -1.0dB

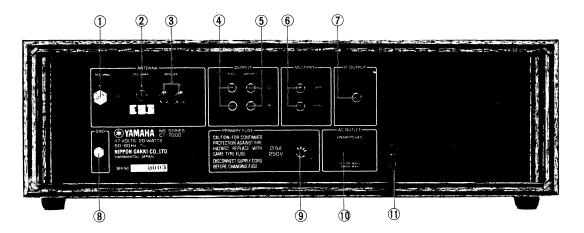
```
Sub-Carrier Suppression
            Over 70dB
Muting Override Signal Level
            3-30µV
Stereo Level 3-30µV
Auto Blend Level
            1:100µV, II:1mV
GENERAL
Transistors
            108
FETs
            12 (dual-gate MOS: 3,
            iunction: 9)
Diodes
            33
Zener Diodes 9
ICs
             7
Two-Element Ceramic Filter
                                     1
Four-Element Ceramic Filter
                                     1
Two-Element Ceramic Block Filters
                                     2
Headphone Output
            4.16Ω
Fixed Output
            775mV
Variable Output
            2V ~ 70mV variable
Antenna Input Impedance
            300\Omega (balanced),
             75\Omega (unbalanced)
Power Source
            110, 117, 130, 220, 240V,
            50/60Hz
Power Consumption
            23W (illum. off: 13W)
AC Outlet
            1 (unswitched, 500W)
Cabinet
             American walnut
Front Panel Hairline brushed aluminum
Dimensions (WxHxD)
            436 x 144 x 352mm
             (17.2" x 5.7" x 13.9")
Weight
             13kg (28.6 lbs.)
```

# **EXTERNAL VIEW**

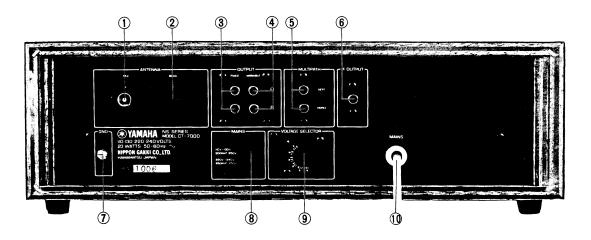
## FRONT PANEL



# REAR PANEL (U.S.& CANADIAN MODELS)



**REAR PANEL (EUROPEAN MODEL)** 



#### FRONT PANEL

- FRONT PANEL
- 2 DIAL SCALE
- O DIAL POINTER
- STEREO INDICATOR
- 6 STATION INDICATOR
- 6 SIGNAL METER
- **1** TUNING METER
- CONTROL PANEL
- HEADPHONE JACK
- HEADPHONE VOLUME CONTROL
  TUNING KNOB

- **1** OUTPUT LEVEL CONTROL
- MUTING LEVEL CONTROL
- MUTING SWITCH
- IF MODE SWITCH
- METER-DISPLAY SWITCH
- AUTO-BLEND SWITCH
- MODE SWITCH
- ILLUMINATION SWITCH
- POWER SWITCH

## **REAR PANEL (U.S.& CANADIAN MODELS)**

- F-TYPE RECEPTACLE
- 2 75Ω ANTENNA TERMINAL
- **300** $\Omega$  ANTENNA TERMINAL
- I FIXED OUTPUT JACKS
- VARIABLE OUTPUT JACKS
- 6 MULTIPATH JACKS

- **1** IF OUTPUT JACK
- **8** GROUND TERMINAL
- **9** FUSE HOLDER
- AC OUTLET (UNSWITCHED)
- AC CORD

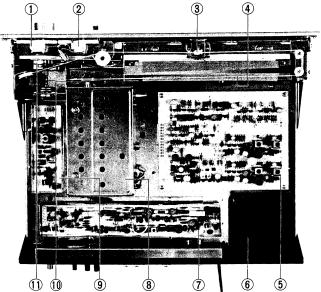
## **REAR PANEL (EUROPEAN MODEL)**

- ANTENNA SOCKET FOR 75 $\Omega$  COAXIAL CABLE
- 2 300Ω DIN ANTENNA SOCKET
- 6 FIXED OUTPUT JACKS
- VARIABLE OUTPUT JACKS
- **6** MULTIPATH JACKS

- **6** IF OUTPUT JACKS
- **7** GROUND TERMINAL
- **B** FUSE HOLDER
- **9** VOLTAGE SELECTOR
- AC CORD

# **INTERNAL VIEW**

## TOP VIEW



- **1** TUNING METER
- **2** SIGNAL METER
- **3** DIAL POINTER

**8** RF PACK

- **4** DIAL LAMP CIRCUIT BOARD

- MULTIPLEX DEMODULATOR

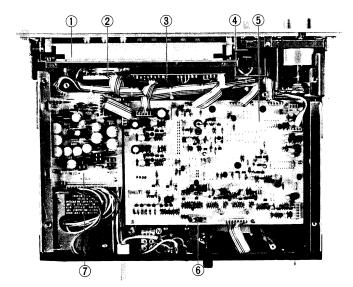
IF AMPLIFIER CIRCUIT BOARD

**9** DISCRIMINATOR CIRCUIT BOARD METER LAMP CIRCUIT BOARD PULLEY FOR VARIABLE CAPACITOR

**6** POWER TRANSFORMER

- CIRCUIT BOARD

# **BOTTOM VIEW**



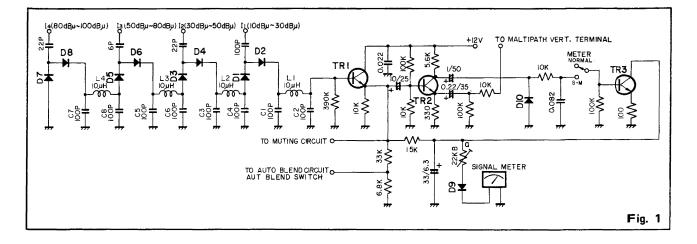
- CONTROL UNIT
- **2** VOLUME CIRCUIT BOARD
- SWITCH CIRCUIT BOARD
- **4** POWER SWITCH
- **O** CONTROL CIRCUIT BOARD
- **6** DE-EMPHASIS SWITCH
- POWER SUPPLY CIRCUIT BOARD

# **CIRCUIT DESCRIPTION**

#### METER CIRCUIT

This circuit has two functions. One permits almost direct feeding of signals with strengths from  $10dB\mu$  to  $100dB\mu$  to the Signal meter. The other permits selection of AM components in the FM signal, thus showing the multipath element and displaying it as a

Signal-minus-Multipath deflection. This system permits the owner to find the best balance of strong and "clean" signals by switching between the two functions of this meter while experimenting with different antenna locations.



#### Operation

#### 1. Signal Display

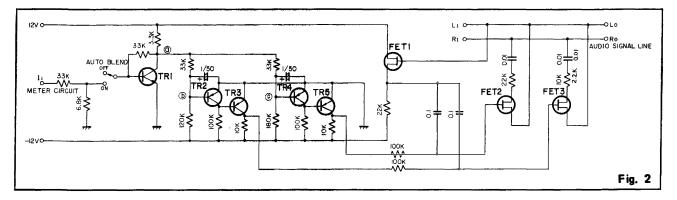
IF stage signals are detected at terminals  $I_1 \sim I_4$ , and each is rectified to double voltage through the actions of two diodes, then sent to Tr1. The signal strength detection circuits are divided into a series of four. For example, when a 10dB $\mu$  signal is received, the IF stage amplifier connected to the I1 terminal generates a signal operating diodes D1 and D2 for the output. That means that the D1, D2, C1, C2, L1 circuit is not operating at this time. When a stronger signal is received, input from I1 rises above 10dB $\mu$  to saturation point (app. 30dB $\mu$ ), at which time input is transferred to I2 to cover the 30dB $\mu$ -50dB $\mu$  range. The I3 terminal is for signals between 50dB $\mu$  and 80dB $\mu$ , while I4 covers the range from  $80dB\mu$  and  $100dB\mu$  This assures excellent, linear meter response. These circuits – L1L2L3L4 (C1-C8) are connected with DC current, not AC.

#### 2. Multipath Display

The Signal meter display signal is amplified at Tr1 to operate the meter. At the same time, the AC element in the signal which shows multipath strength is amplified and rectified to a half wave by D10. In this way positive potential is obtained. At this time, if the meter switch is set to S-M, positive potential is fed to Tr3 (in parallel with the signal meter) lowering the impedance of Tr3 and reducing the Signal meter amplification accordingly.

#### AUTO BLEND CIRCUIT

A weak stereo signal is accompanied by noticeably more noise than a monophonic signal of the same strength. The phase of such noise is opposed in the right and left channels, so it is possible to cancel it, at least in part, through mixing the channels, without losing frequency response. The mixing is done automatically by this circuit, in two stages: for signals below  $60dB\mu$  and below  $40dB\mu$ . Naturally, the stronger the blend effect, the less stereo separation is available. (Separation  $40dB\mu$  1KHz = 8dB,  $60dB\mu$  1KHz = 14dB)



#### Operation

1. With the Auto Blend Switch Off

Since the Tr1 bias is positive, Tr1 switches on and E potential at point (a) is zero. Potentials at points (b) and (c) are below -0.5V, so Tr2 and Tr4 are on, Tr3 and Tr5 off. In addition -12V is fed to the gates of FET1 FET2, so there is no blend effect.

#### 2. With the Auto Blend Switch On

a. If the strength of the received signal is less than 40dB $\mu$ , potential present at input terminal I<sub>1</sub> is low, so that the output impedance from Tr1 is high, raising the potentials at points (b) and (c) to more than -0.5V. This switches Tr2 and Tr4 off, and Tr3 and Tr5 on, and potential almost the value

of E is fed to FET2 and FET3 gates. In addition, the left channel audio signal is fed from FET1 to the FET2 and FET3 gates for a steady blend effect in both stages.

- b. When the strength of the received signal is below  $60dB\mu$  but over  $40dB\mu$ , Tr1 impedance lowers and potential at point(a) is enough to cause the potential at points (b) and (c) to drop, below -0.5V, so that the blend effect is available, but only via FET3.
- c. When the strength of the received signal is over  $60dB\mu$  Tr1 is almost switched on, which is the same condition as that described in (1) above. This cancels the blend effect.

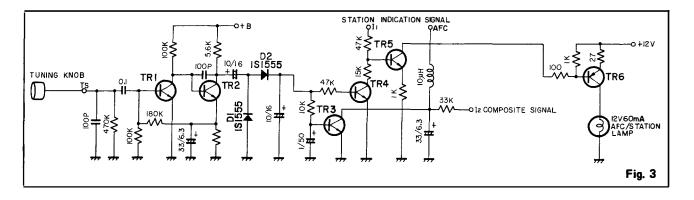
# AUTO TOUCH AFC OFF CIRCUIT & STATION/AFC INDICATOR CIRCUIT

These circuits turn the AFC off when a station is being tuned, and also indicate when tuning is in the reception area of a station, showing that the AFC is off at the same time. This gives rise to the following merits:

1. If the AFC is constantly operating in the tuner then, assuming that AFC-caused frequency drift is 1/n, the apparent selection zone is increased n times. In such a case, if there is a powerful station nearby on the dial, the tuning is apt to be pulled off, even during station selection, which will make it impossible to tune the nearby station. To avoid this problem the AFC goes off automatically when the tuning knob is touched.

2. More recent tuners tend to do away with the AFC function altogether, depending upon advanced circuit technology to suppress drift. In these tuners, circuits are designed to resist the affect of temperature fluctuations, so that drift can be forgotten as a serious problem.

In such a tuner, the addition of AFC will of course provide extra protection against drift – and this is the superior feature of the Yamaha system.



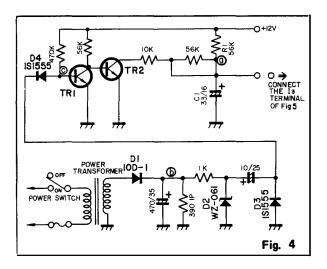
#### Operation

1. Touching the tuning knob with your finger passes the electric potential in the human body to the TS terminal, and it is amplified by Tr1 and Tr2, then rectified by D1 and D2 diodes for (+) potential. 2. This potential is added to the base of Tr3, switching it on and grounding the AFC terminal (i.e., canceling the AFC effect). 3. At the same time, this potential is fed to the base of Tr4, so that that transistor comes on and grounds Tr5 with  $15k\Omega$ .

4. When the station display signal is fed to the I<sub>1</sub> terminal, Tr5 comes on and switches Tr6 on. This lights the AFC/Station lamp. At this time, if operation (3) above is added, Tr5 output voltage drops and Tr6 output impedance rises, so that the AFC/Station lamp dims to show that the AFC is off.

#### TRANSIENT NOISE CANCELATION CIRCUIT

This circuit serves to cancel the noise and distortion caused by turning the Power switch on and off.



#### Operation

1. When the Power switch is turned on

When the power is switched on, potential at point (a) is equal to E, and rises to +12V according to a time constant decided by C1R1. When this +12V is added to the base of Tr1 this transistor switches on and shuts Tr2 off, so that the difference in voltage obtained by C1R1 appears as it is at terminal 0. This switches Tr7 (previous diagram) on, and the same condition continues until the voltage at point (a) reaches +11.5V, thus cancelling unwanted signals at the time power is switched on.

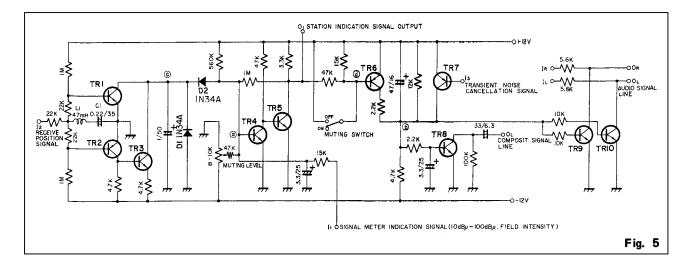
#### 2. When the Power switch is turned off

When the power is switched off voltage at point D changes from 12V to E potential. This change is rectified to couple voltage by D3D4, and negative load potential is obtained at point C. This switches Tr1 off and Tr2 comes on, so that E potential is present at the collector of Tr2 and the negative potential change is available at terminal 0, providing a muting effect at the instant the power is switched pf.

D2 removes the ripple in the negative voltage at point D, which means it cancels the operation of this circuit, which works via ripples when the power switch is turned on.

#### MUTING CIRCUIT

This circuit eliminates weak signals and inter-station noise during tuning, or in case of station drift. This circuit also functions to cut transient noises, a function which will be explained in another section.



#### Operation

- 1. Muting operation due to a signal from terminal I1.
- a. DC voltage e1, which shows the strength of the received signal between  $10dB\mu$  and  $100dB\mu$  is fed to the I1 terminal and passes to the base of Tr4, which has had its bias adjusted by VR1.
- b. If e1, is less than the set bias value, Tr4 switches off, switching Tr5 and Tr6 on. This makes the voltage at point () sufficient to turn on Tr8, Tr9 and Tr10, grounding the composite signal via Tr8 and the audio signal via Tr9 and Tr10.
- c. On the other hand, if e1 is greater than the set bias value, the Tr4 switches on, thus switching off Tr5 and Tr6. This means that the voltage at point will switch off Tr8, Tr9 and Tr10, thus passing the composite and audio signals.
- 2. Muting operation due to a signal from terminal I2.
- a. Signal  $e^2$ , which contains a DC component showing the tuning aberration, is fed to the I<sub>2</sub> terminal. This is potential which can be obtained by the discriminator S curve. When the received signal is lower than the correct tuning point, negative volt-

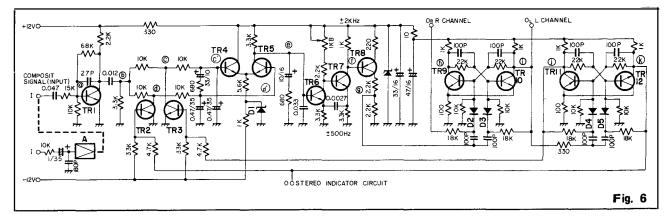
age is contained in  $e^2$ ; when it is higher, positive voltage is present.

- b. When the received signal is lower than the correct tuning point, Tr1 is continually off, but Tr2 can be switched on by voltage of below -0.5V, and at this time Tr3 comes on. In this case potential at point © becomes zero due to diode D1, while point ③ is grounded by D2, thus switching on the muting.
- c. When the signal is higher than the muting point, Tr2 and Tr3 are continually off. Tr1 comes on when it receives more than -0.5V, creating E potential at point ©. D2 thus grounds point ⓐ, switching on the muting.
- d. In addition to selecting DC voltage, this circuit works in the same way to block unwanted signals of below 20Hz and over 50kHz through the actions of C1 and L1. This cuts the noises normally heard when the tuning knob is turned quickly and passes one or more stations.
- e. When the muting effect is not desired it can be bypassed by raising voltage at point (d) to +12V via the Muting switch, thus turning Tr6 off.

#### PHASE LOCKED LOOP CIRCUIT

This circuit accurately creates a switching signal which divides the stereo components of the FM signal. It is divided into two parts: an oscillator and a section

to match the oscillator 19kHz pilot signal to the broadcast pilot signal.



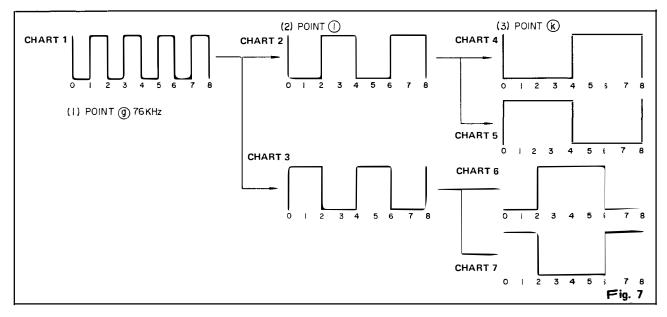
#### Operation

- 1. Oscillator Section
- a. A 76kHz sine wave is obtained from the VCO formed by Tr6 and Tr7. It is changed into a square wave at Tr8 and then fed to the doubled multivibrator formed by Tr9 and Tr10. Thus at OR and OL 38kHz square waves of opposite phase are obtained.

At the same time this 38kHz signal is fed to the

doubled multivibrator formed by Tr11 and Tr12, creating 19kHz square waves of opposed phase at points  $\bigcirc$  and O. Waveforms and phase relations at each time point are as shown below:

b. As a principle of operation, the oscillation frequency (phase) of the VCO rises (advances) as the Tr6 potential becomes more positive, and drops (retards) as it becomes more negative.

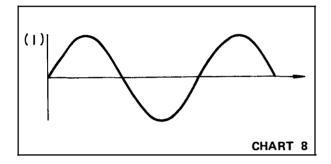


Notes:

1. Waveforms at point (b) are the same square wave as at point (c), but phasing is opposite.

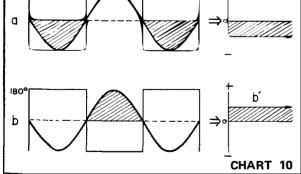
2. The waveform at point () is the same square wave as that at point (), but the phase is (p posite.

- 2. Phase Comparison and Detection Section
- a. The 19kHz pilot signal, a composite signal necessary for phase comparison and detection, is selected by the band pass filter composed of the circuit from the l' terminal to point (a).
- b. This selected 19kHz pilot signal is amplified by Tr1 and fed to points © and ©, connected to Tr2 and Tr3 respectively. To these transistors the 19kHz square wave from the oscillation section points (j) and <sup>®</sup> are also fed.
- c. In this way the phase comparison and detection circuit is formed, and output Vc' and Vd' can be



2-1. To detect the waveform in chart 9 above according to chart 4 and chart 5 (previous page),
10-a. Output at (d) when chart 4 is added to Tr2.
10-b. Output at (c) when chart 5 is added to Tr3. At the same time, these also show the outputs at point
(c) (in the condition seen in chart 10-(a)) when chart 4

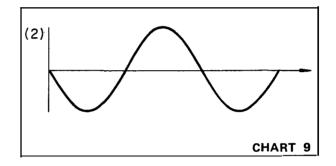
is set to Tr3, also show the output at the point  $(\mathbf{d})$ in chart 10-(b) when chart 5 set to Tr2.



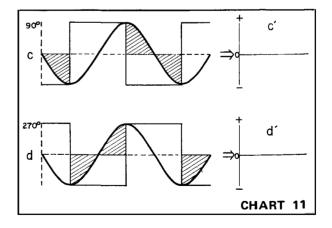
obtained. Since the 19kHz square wave signals driving Tr2 and Tr3 are in opposite phase, Vc' and Vd' are activated alternately. This phenomenon is then amplified by the differential amplifier formed of Tr4 and Tr5.

d. The phase relations between the pilot signal and the signals at points (j) and (k), as well as the potential at points (c') and (d), are shown below:

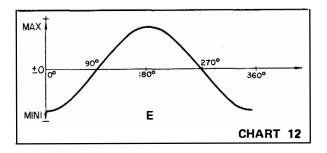
The figure to the left shows the point (a) input waveform. The one on the right shows that for point (b).



2-2. To detect the waveform in chart 9 above using chart 6 and chart 7 shown on the previous page: 11-d. Output at (d) when chart 6 is added to Tr2. 11-d. Output at (o) when chart 7 is added to Tr3. At the same time, these also show the outputs at point  $\bigcirc$  (in the condition seen in chart 11- $\bigcirc$ ) when chart 6 is set to Tr3, also show the output at the point  $\bigcirc$  in chart 11- $\bigcirc$  when chart 7 set to Tr2.



2-3. Potential at points  $\bigcirc$  and  $\bigcirc$  becomes that shown at a' b' c' and d' in the chart 10 and 11. Phase differences and output relations between pilot signal and detection signal are as shown in the following chart 12.



2-4. Since points (c) and (d) are connected to the differential amplifier, its operation creates the following relations. Let the potential at (c) be E(c), at (d), E(d) and at (e), E(e). Then E(e)=K(E(c) - E(d)). Furthermore K>0. The output potential at e undergoes the change shown in chart 12, also due to the phase difference between the pilot signal and detection signal j added to Tr3.

3. Oscillation Section and Phase Comparison Detection Section

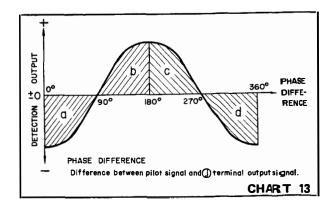
The output potential of the phase comparison detection section is added to the base of Tr6 in the VCO, so that by examining 1.b and 2-4 one can see that a locked condition is possible only when the phase difference is either  $90^{\circ}$  or  $270^{\circ}$ , and the following conditions hold:

- a zone: VCO works for phase retard.
- b zone: VCO works for phase advance.
- c zone: VCO works for phase advance.
- d zone: VCO works for phase retard.

Therefore the only stable, locked condition is  $270^{\circ}$  phase difference (with a  $90^{\circ}$  difference, even a tiny retard will cause an automatic shift to  $270^{\circ}$ ).

Therefore, according to the charts in the Oscillator Section discussions, what leads to stability are j terminal output (chart 7), (k) terminal output (chart 6), i) terminal output (chart 3) and (h) terminal output (chart 2).

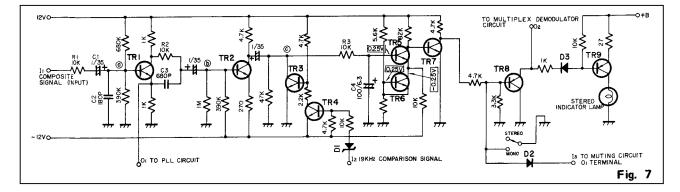
Even if a locked condition arises at  $90^{\circ}$  phase difference, it will be due to(i) terminal output (chart 3) and (h) terminal output (chart 2), so that switching will not be affected at all.



# STEREO/MONO DRIVE AND STEREO INDICATOR CIRCUIT

This circuit receives stereo FM signals strong enough for stereo reception. When the PLL circuit is phase

locked the demodulator operates and the stereo indicator lamp lights, which is this circuit's function.



#### Operation

1. The composite signal is fed from the  $I_1$  terminal and undergoes a high cut due to R1, C1 and C2. It is added to the 19kHz 90° phase shifter composed of Tr1, R2 and C3. In this way the 19kHz pilot signal leads by 90° and is amplified at Tr2 (only during stereo broadcast reception, of course).

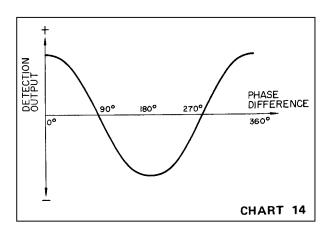
2. At the same time, the PLL circuit D terminal output (7) enters via I  $_2$  and the phase detector Tr3 is operated by Tr4.

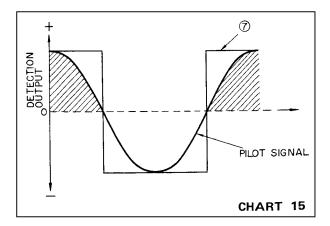
3. Using the phase difference between the pilot signal and signal, Fig. 7 to see the potential at point  $\bigcirc$  (i.e., the phase detector output, the following points become clear. See chart 14.

The operation to the detector when the PLL circuit is phase locked is shown by the chart 15. At this time the phase difference is zero, and maximum positive output is obtained.

4. The potential change is integrated by R3 and R4, and when it is more than 0.25V, Tr5 goes on and Tr7 goes off, which switches Tr8 on. In this way the multiplex demodulator begins to operate and at the same time the stereo indicator lamp lights, driven by Tr9.

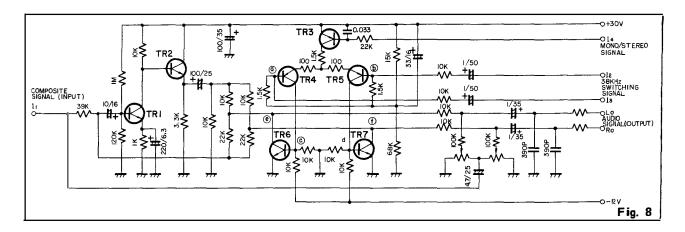
However, if the station display signal is not received via  $I_3$ , i.e., if the strength of the received signal is below the muting level, the base of Tr8 will be grounded through D2, keeping it off and keeping the stereo indicator lamp from lighting. The multiplex demodulator will also fail to be activated.





#### MULTIPLEX DEMODULATOR CIRCUIT

This circuit double tunes the composite signal by a switching method, thus separating out the left and right channel audio signals.



#### Operation

The composite signal entering at  $I_1$  is amplified by Tr1 and becomes output due to the low impedance of the Tr2 emitter-follower circuit.

Also, via the I<sub>2</sub> and I<sub>3</sub> terminals a 38kHz square wave is constantly received from the PLL circuit. At terminal I<sub>4</sub> a minus potential is received only when the signal is in stereo.

Therefore, when a stereo signal is received Tr3 comes on. Tr4 and Tr5 repeat seesaw on-off motions at the 38kHz frequency, which drives Tr6 and Tr7. When Tr4 is on, Tr6 comes on (at the same time Tr5 is off, switching Tr7 off). When Tr4 turns off, it switches off Tr6 (at this time Tr5 is on and thus so is Tr7). In this way the grounded-open movement is repeated between points (a) and (f) at the 38kHz rate, permitting the right and left audio signals to be obtained from the composite signal. One part of the audic signal is fed back through terminal I1 for more perfect separation characteristics. In addition, when a monophonic signal is being received, Tr3 shuts off, and this turns off Tr4, Tr5, Tr6 and Tr7, so that the audio signal obtained at terminals Lo and Ro are monophonic.

# PARTIAL DISASSEMBLY

### 1. Cabinet Removal

1-1. Turn the unit over and remove screws  $(1\sim 4)$ . See Photo 1.

1-2. With the unit as shown in Photo 2, remove the chassis from the cabinet.

## 2. Control Circuit Board Removal

2-1. Remove the cabinet.

2-2. Remove the screws  $(1\sim 5)$  shown in Photo 3, then take off the cover.

### 3. Power Supply Circuit Board Removal

3-1. Follow steps 2-1 and 2-2.

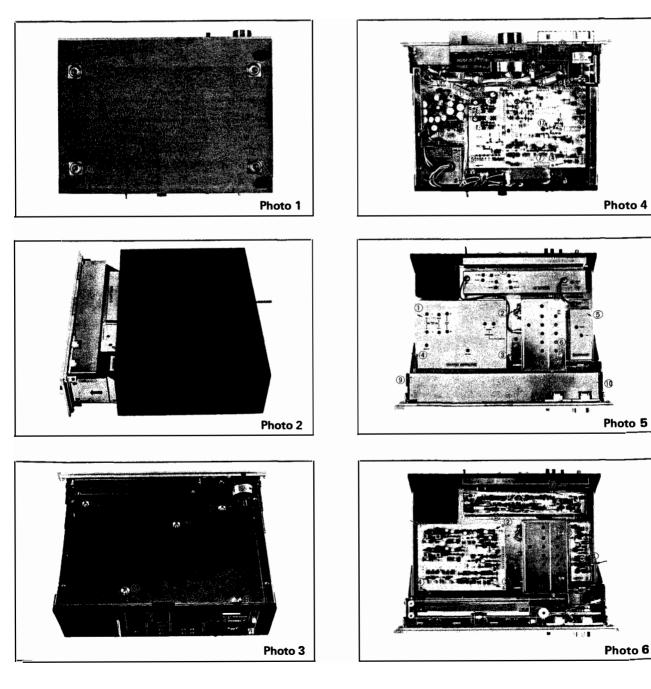
3-2. Remove connectors  $(8\sim10)$  and screws  $(7\sim10)$  shown in Photo 4, then remove the power circuit board.

#### 4. Power Transformer Removal

4-1. Follow steps 2-1 and 2-2.

4-2. Take off connector (10) shown in Photo 4, then remove the primary side leads.

4-3. Remove screws  $(11 \sim 14)$  shown in Photo 4, while pressing on the top of the transformer, then remove the transformer.



#### 5. Multiplex Demodulator Circuit Board Removal

5-1. Remove the cabinet.

5-2. Remove screws  $(1 \sim 4)$  shown in Photo 5, then take off the shield cover.

5-3. Remove screws  $(1\sim 4)$  shown in Photo 6, then take out the MPX circuit board.

#### 6. Discriminator Circuit Board Removal

- 6-1. Remove the cabinet.
- 6-2. Disconnect the input pin plugs.
- 6-3. Remove screws (5) and (6) shown in Photo 5, then take off the shield cover.

6-4. Remove screws (5) and (6) shown in Photo 6, then take out the discriminator circuit board.

#### 7. Amplifier Circuit Board Removal

- 7-1. Remove the cabinet.
- 7-2. Pull out the input and output pin plugs.

7-3. Remove screws (7) and (8) shown in Photo 5, then take off the shield cover.

7-4. Remove screws (7) and (8) shown in Photo 6, then take out the IF circuit board.

#### 8. RF Pack Removal

8-1. Remove the cabinet.

8-2. Loosen screws (9) and (10) shown in Photo 5, screw (3) shown in Photo 11 and screw (3) shown in Photo 12, then take off the shield cover.

8-3. Loosen the two variable capacitor pulley fixing screws, then remove the VC pulley.

8-4. Unplug the input and output pin plugs, then disconnect the connector.

8-5. Remove screws (15 $\sim$ 17) shown in Photo 4, then take out the RF pack.

#### 9. Meter Lamp Circuit Board Removal

9-1. Remove screw (1) shown in Photo 7, then remove the meter lamp circuit board.

#### 10. Dial Lamp Circuit Board Removal

10-1. Remove screws (1) and (2) shown in Photo 8, then remove the dial string cover.

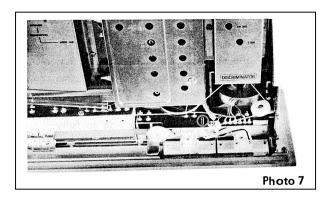
10-2. Remove screws (1) and (2) shown in Photo 9, then remove the dial lamp circuit board cover.

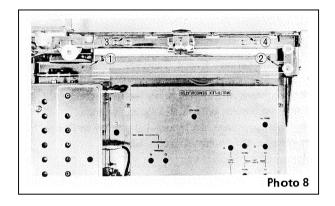
10-3. Remove screws (1) and (2) shown in Photo 10, then take out the dial lamp circuit board.

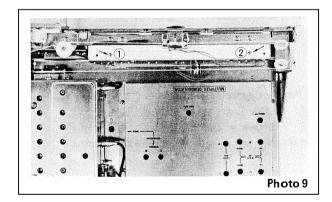
#### 11. Dial Pointer Removal

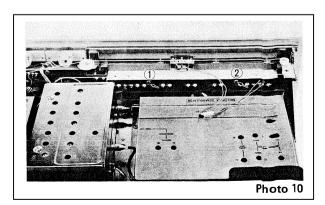
11-1. Follow step 10-1.

11-2. Remove screws (3) and (4) shown in Photo 8, then separate the guide rail from the pointer.









## 12. Front Panel Removal

12-1. Follow steps 2-1 and 2-2.

12-2. Remove connectors  $(2\sim5)$ , (8) and (11) shown in Photo 4, then remove the switch circuit board. 12-3. Loosen the two tuning knob fixing screws shown in Photo 4 in the direction of the arrow, using a hexagonal wrench. Remove the tuning knob.

Note: The shaft can be loosened; do so fully.

12-4. Remove screw (18) shown in Photo 4.

12-5. Remove screws (1) and (2) shown in Photo 11, and screws (1) and (2) shown in Photo 12.

12-6. Remove the front panel and control unit without separating them. Remove screws  $(1\sim 4)$  shown in Photo 13, then separate the front panel and control unit.

#### 13. Volume Circuit Board Removal

13-1. Follow steps 12-1 through 12-5.13-2. Remove the headphone volume, output level and muting knobs with a hexagonal wrench.13-3. Remove screws (1) and (2) shown in Photo 14, then remove the volume circuit board.

#### 14. Switch Circuit Board Removal

14-1. Follow steps 12-1 through 12-5. 14-2. Remove screws (3) and (4) shown in Photo 14, then remove the switch circuit board.

#### 15. Power Switch Removal

15-1. Follow steps 12-1 through 12-4. 15-2. Remove screws (1) and (2) shown in Photo 15, then remove the power switch circuit board.

#### 16. Meter Removal

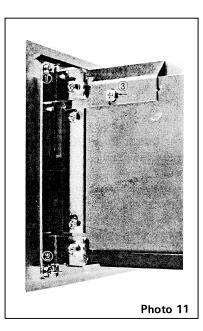
- 16-1. Follow steps 12-1 through 12-4.
- 16-2. Follow step 8-2.
- 16-3. Remove the leads connected to the meter.

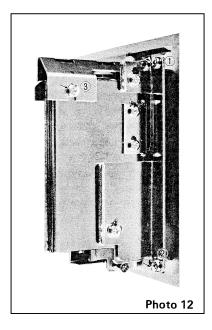
16-4. Remove screws (3) and (4) shown in Photo 15,

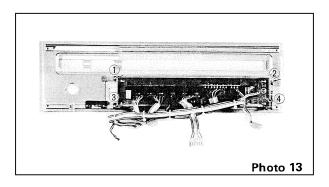
- then remove the meter cover.
- 16-5. Take out the meter.

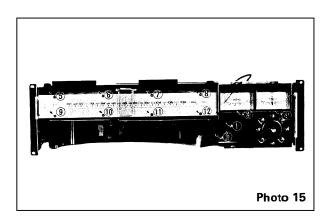
#### 17. Dial Scale Removal

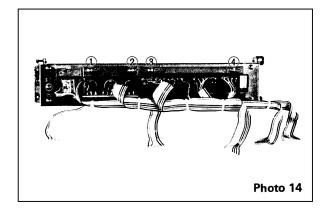
17-1. Follow steps 12-1 through 12-4. 17-2. Remove screws (5 $\sim$ 12) shown in Photo 15, then remove the dial scale.



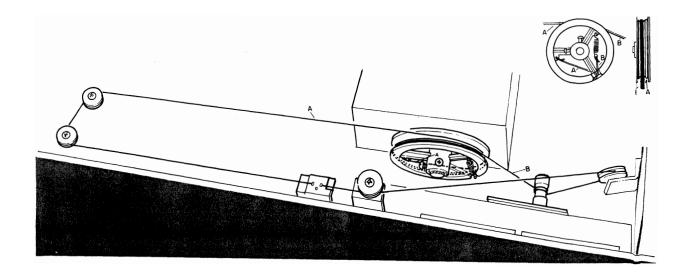








# DIAL MECHANISM



# MEASUREMENT AND ADJUSTMENT

## **1. TUNER SECTION**

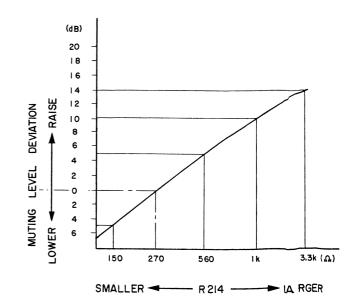
Before Adjustment . . . . After 30 minutes aging with power switch on and each circuit board cover removed.

STEP	ADJUSTMENT ITEM	TERMINAL TO BE CONNECTED & INSTRUMENT REQUIRED	ADJUSTMENT	HOW TO ADJUST	RATING OR STANDARD	REMARKS
1	Discriminator Balance (Normal)	Received signal frequency near center, antenna unconnected.	T301: discriminator coil Secondary side (upper) core	Move the core from left to right until the center meter indicates "O", then fix it in place.	INSIDE THIS MARK	When the power switch is turned off be sure the meter reading mechanical indicates "O". See Note 3.
2	IF Core Preset	Antenna Terminal (300Ω) FM signal generator 98MHz/60dBμ	RF Pack IF core	Move the core to the right and left to find the location which af- fords maximum signal meter needle deflection.		Set at the same loca- tion which causes the center meter to indicate "O".
3	Monaural Dis- tortion Ratio	Antenna terminal (300Ω) FM signal generator 98MHz/60dBμ Modulation: 400Hz/100% mono Fixed output jacks (L, R) Oscilloscope, Electronic volt- meter, Distortion ratio meter		Move the core to the left and right and set at the location affording minimum distortion. Test at both Normal and Wide modes.	-62dB (0.08%)	If Normal and Wide different, adjust bal- ance.
4	VCO Adjustment	Antenna terminal (300Ω) FM signal generator 98MHz/60dBµ Unmodulated MPX circuit board 19kHz T.P Frequency Counter	MPX circuit board VCO adjustment	Set to 19kHz.	19.000kHz± 20Hz (5Hz)	
5	Stereo Distor- tion Ratio I (Normal)	Antenna terminal (300Ω) FM signal generator 98MHz/60dBμ, 100dBμ Modulation 400Hz/100% stereo Fixed output jack (L, R) Oscilloscope, Electronic volt- meter, Distortion ratio meter	IF circuit board VR201, 202 TC201, 202	Set for minimum dis- tortion.	60dB <i>µ</i> , —62dB IF MODE = 100 dBµ, —57	NORMAL
6	Stereo Distor- tion Ratio 11 (Wide)	Same as Step 5	RF pack IF core, RF2~5 core If standards not met, repeat Step 5.	Adjust for minimum distortion within limits that signal meter indi- cation does not drop (with $60dB\mu$ antenna input).	60dBµ 62dB, (0.08%) 100dBµ 57dB, (0.14%)	Carry out this step if the Step 5 check does not fall within the 100dBµ limits.
7	Stereo Distor- tion Ratio III (Wide)	Same as Step 6	IF circuit board VR203, 204 TC203	Set for minimum dis- tortion.	-62dB (0.08%)	
8	Wide Balance	Same as Step 1	IF circuit board T201 coil	Set for "O" center meter reading.	Same as Step 1.	Repeat the check in Step 7. If the read- ing is off consider- ably, repeat Steps 7 & 8.
9	Separation I (Normal)	Same as Step 6.	MPX circuit board Separation Adj. Normal L, R	Set L & R for maxi- mum separation. (both L & R)	55dB	
10	Output Level	Same as Step 6.	MPX circuit board Level Adj. L, R	Set for limits shown at right.	−1dBm ± 0.3dB	
11	Separation II (Normal)	Check whether St	ep 10 has caused a	change in the Step 9 valu	ies; if so, repeat	Step 9.
12	Separation III (Wide)	Same as Step 6.	Control circuit board Separation Adj. Wide	Set for maximum sepa- ration.	55dB	Adjust for balance if difference between L&R.

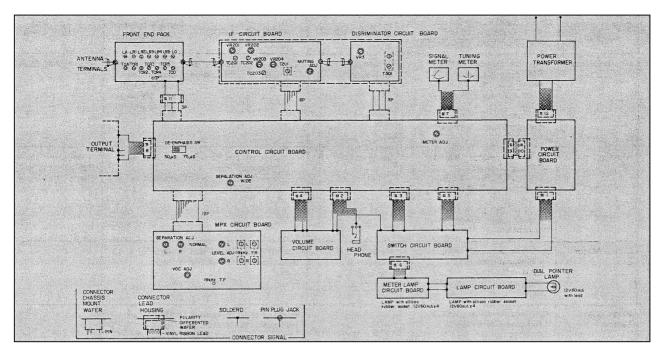
				1		1
STEP	ADJUSTMENT ITEM	TERMINAL TO BE CONNECTED & INSTRUMENT REQUIRED	ADJUSTMENT	HOW TO ADJUST	RATING OR STANDARD	REMARKS
13	Muting Level I	Antenna terminal (300Ω) FM signal generator 98MHz/30dBμ Modulation 400Hz/30% mono Fixed output jacks (L, R) Oscilloscope, Electronic voltmeter	IF circuit board Muting Adj.	Turn slowly from L to R and set at point where output begins.	Once set, dif- ference from signal fed by FM SG must be 30dBµ ± 6dBµ	Muting VR=30dBµ
14	Muting Level	Antenna terminal (300Ω) FM signal generator 98MHz/30dBμ, 10dBμ Modulation 400Hz/30% mono Fixed output jacks (L, R) Oscilloscope, Electronic voltmeter	Same as Step 13.	Set for 30dBμ ± 6dB Muting VR. Set for 11dBμ ± 5dB Muting VR.		If these values can- not be obtained, check the Note 1.
15	Signal Meter Set	Antenna terminal (300Ω) FM signal generator 98MHz/60dBμ Modulation 400Hz/30% mono	Control circuit board Meter Adj.	Set so that the needle deflects to "60" on the meter.	60 ± 5	
16	Auto Blend Check	Antenna terminal (300Ω) FM signal generator 98MHz Modulation 1kHz/100% stereo Fixed output jacks (L, R) Oscilloscope, Electronic voltmeter		After FM SG output reduced, check change in separation and final separation.	Operation Level 1:60dBµ ± 10dBµ 11:40dBµ ± 10dBµ Separation 1:14dB ± 3dB 11:8dB ± 3dB	
17	IHF Sensitivity Check	Antenna terminal (300Ω) FM signal generator 98MHz Modulation 400Hz/100% mono		Check antenna input when distortion ratio is - 30dB (3.2%).	8dBμ (2.5μV) (6dBμ (2.0μV))	If these values can- not be met, see Note 2.
18	Dial Pointer Set	Antenna terminal (300Ω) FM signal generator 90, 98, 106MHz	Dial pointer	Set the dial pointer so that it is at the correct dial location for each frequency.	± 1mm	98MHzstandard

## NOTES

- 1. If the required values in Step 14 cannot be met after the muting level is set to  $10dB\mu$ , correct by changing R214 & 270 in the IF circuit board as shown in the figure to the right. Then carry out all steps after Step 15.
- When carrying out the IHF sensitivity check in Step 17, if the results do not satisfy the requirements adjust each RF pack core (LA & LR1~5), and each trimmer (TCA & TCR1~5) for maximum sensitivity. For this adjustment, however, first repeat the adjustments and checks beginning with Step 1.
- 3. When the power switch is off, if the center meter does not indicates "O", adjust it mechanically to this point via the adjustment hole on the back of the meter. Then carry out the check in Step 1 again.



## ADJUSTMENT POINT



# 2. PRECAUTIONS AND STEPS IN CORRECTING FAULTS

#### 1. Replacing Parts

1-1. When it is necessary to replace any parts in the RF pack, or an IC, transistor or discriminator coil in the discriminator circuit board, change the whole unit, not the individual part.

1-2. When adjustable parts other than those mentioned in 1-1 above are replaced, such adjustable parts (vari-

able resistors, adjustable trimmers and inductors) must be readjusted as explained in part 2 below.

1-3. When non-adjustable parts other than those mentioned in 1-1 and 1-2 above are replaced, no adjustment is necessary. Be sure, however, to replace only with parts meeting required specifications whenever such specifications are listed.

## 2. Unit Replacement

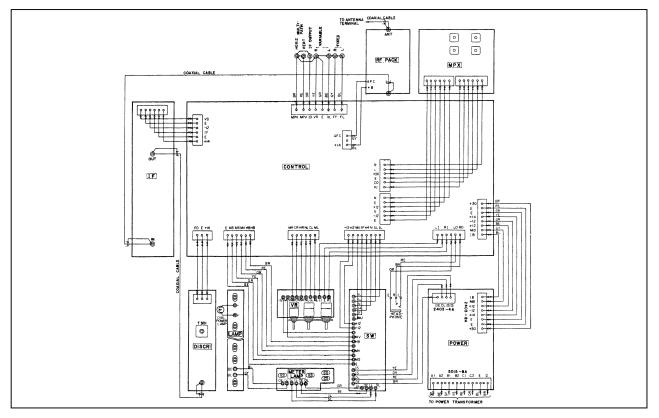
UNITNAME	ADJUSTMENT ITEM	REQUIRED MEASUREMENT INSTRUMENT
RF Pack	Overall adjustment (only when indi- cator set point is out of adjustment)	Ultra-low distortion FM signal generator Standard signal generator Distortion meter Oscilloscope
IF Circuit Board	Overall adjustment	Electronic Voltmeter
Discriminator Circuit Board	Overall adjustment	
MPX Circuit Board	<ol> <li>VCO Adjust : 19 kHz</li> <li>Separation Adjust (Normal)</li> <li>Output Level Adjust</li> <li>Separation Adjust (Wide)</li> </ol>	Ultra-low distortion FM signal generator Standard signal generator Oscilloscope Electronic Voltmeter Frequency Counter
Control Circuit Board	<ol> <li>Separation Adjust (Wide)</li> <li>Signal Meter Adjust</li> </ol>	Ultra-low distortion FM signal generator Standard signal generator Oscilloscope Electronic Voltmeter
Power Circuit Board	VCO Adjust : 19 kHz	Frequency counter
Tuning Meter	Correct off-center reading with power switch off.	Unnecessary

## 3. SPECIFICATION ASSURANCE CONDITIONS

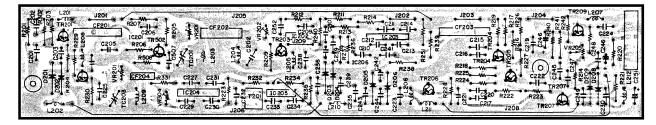
ITEM		MEASUREMENT CONDITION	VALUE
Test Temperature			25 ± 10°C
Test Humidity			65 ± 20°C
AC Input Voltage/Frequency	U.S.	117V	105 ~ 135V 45 ~ 65Hz
	Europe	110V	90 ~ 115V 45 ~ 65Hz
		130V	110 ~ 135V 45 ~ 65Hz
		220V	200 ~ 240V 45 ~ 65Hz
		240V	220 ~ 260V 45 ~ 65Hz
Vibration Noise			Low enough to be no test influence.
Aging Time	At stand	ard condition before test	more than 30 minutes.
Test System Performance	400⊦ 30 ~ Test Sys 100 ∕	Accuracy, Deviation and Precision	Within ± 0.3dB Within ± 0.1dB Within 0.02% Within 0.05%
	40Ó⊦ 50 ~	tem Separation Iz 10,000Hz Signal Phase	Within 60dB Within 50dB Within ± 0.5°
	Overall 1 Mona Stere		Within 80dB at 30 ~ 15,000Hz/50μS Within 75dB at 30 ~ 15,000Hz/50μS
	Signal G	enerator Output Level Accuracy	Within ± 0.5dB
	Modulat	ion Degree Accuracy (75kHz deviation)	Within ± 3kHz
	Signal G	enerator Spurious	Within -100dB at ± 300kHz~±5MHz
	Residual	FM factor at AM	Within -70dB at FM±75kHz/AM 30%
Radio Frequency Noise	300Ω A	ntenna terminal Equivalent	Within -10dBµ

# PRINTED CIRCUIT BOARD

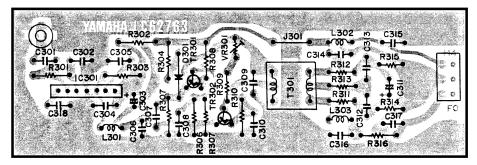
## WIRING CONNECTION DIAGRAM



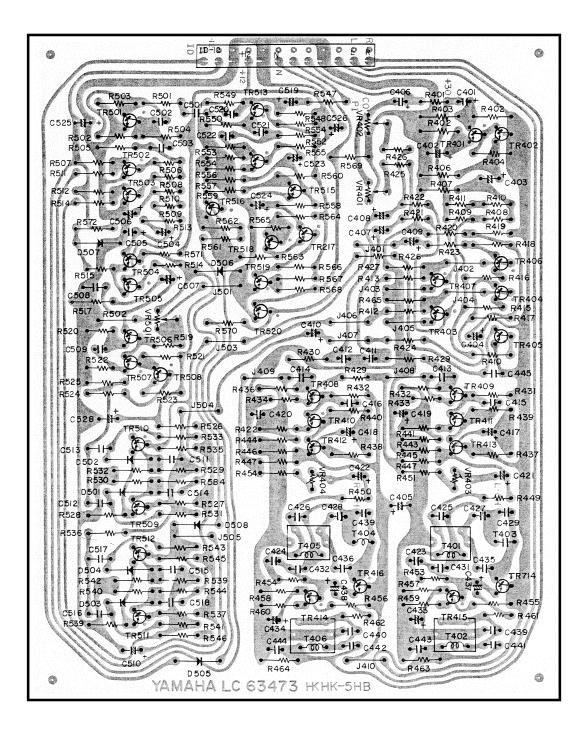
## IF CIRCUIT BOARD NA06480



## DISCRIMINATOR CIRCUIT BOARD NA06482: U.S.& CANADIAN MODELS NA06481: EUROPEAN MODEL



## MPX CIRCUIT BOARD NA06483



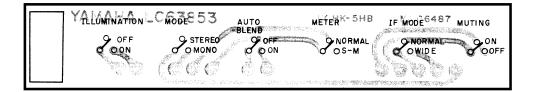
<u>26 G NU PALA UR</u>		子 教育 法国际分析	Wert with the s		
IB MB +12 +12 +14	E +30	a na ana amin'ny fanisa amin'ny fanisa amin'ny fanisa amin'ny fanisa amin'ny fanisa amin'ny fanisa amin'ny fanis		J666 🕈 🛰	
100 - 200 - 200 	Catta Land /	6733	03	a la constante de la constante	R678
and the second	+8+ + <sup>R771</sup>		AHA 3350	C632 TR628	• 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
<b>1</b>	-N	<b>↓</b> •-{}•-{}•-{}·	N AR	ન∰ •હ	R677
8	C736 + C728	C727	第二日本の 気に 愛い	C636 TR627	RG72
6		#/05			2000
			NG 1	8674 TR630	R683
3	R766	• W • • • • • • • • • • • • • • • • • •	P P	R679 W	R682
	TR717 * A TR	R757	R723		• • · · · · · · · · · · · · · · ·
6-		TRTI 🧭 💓 🐄	C723		R680
			R632 R643	R671	• • · · · · · · · · · · · · · · · · · ·
	R760 TR715	R759	51 · · · · · · · · · · · · · · · · · · ·	R678 TR	652 ***
C737	C726-1- WR752	C725	5 J664	AND A	•
( <b>*</b> − <b>N</b> <sup>+</sup> •				C634 18002	
Constant of the second of the	· · · · · · · · · · · · · · · · · · ·	₽747 + C721			el He C632
R720	R748 TR713 C722	TR712 J661	J662 R644		•
649		J660		1002	
1.	C C C C C C C C C C C C C C C C C C C	R6 42'		R631 R672 TR623	
TR632	-12 +12 N PL CO	+30 L R R646	R652	-W-9	
*(•• <b>*</b> )	32 0 0 01010101010		• • · · · · · · · · · · · · · · · · · ·		R663 -1 Hes
	46 J647 J6	48			R66I C623
11 R687 R63L	a second se	R640		•	R659 C622
SC TR6	TD613 6 0618	J668	2000 - B		
M = W			BAT Classical and Court	4 in 1995	<u>្ត្រីទី</u> ពួក្ត្រី
Contraction of the second		J626	L60	C629 TR622	
19 16 16		CAL THE UDIN	🖌   🗞 🌾 🖓 🔶 🕁		0000 0 2
MU R692	R637	- DCZC	V C C C C		KPPO 2 1
					MR656 0 00
+22-56	1000		J654	←1  ← C652	R745 9
-12 D60			ACER A		
AN NG	- H TR634 R644	R633		R731	MR741 0 R
0.00 D6	S States Aller		J652		WR(3)
Di Di	06 R642		R723		MR739
Communities of	• 10 R690	1//// main 1///	R724	R732	M•
and a second	TR633 R689 C	₩ <b>₩</b> 619	W Q		+•N•
	R64L	TR6II	L60I	· · · · · · · · · · · · · · · · · · ·	R727
R72		and the states	45		R729
MU R72	- \\\-			C732 C709 64	ANTER VIL
Ci m			R624		₩ • R746
	603 B628 J637	States and	TRGIO		₩-• R744 ₩-• R742 VR
1 - T A F A	-603 R628 J637	MMM	C R623		1000 TN 5
NR	J635	R634	10-11		W-8738
CR J634	and the second	R625	C 40 TR 609 R620		W- R736 MP
MR 1633	JEST - JEST	👻 🖉 🖓 😽 😽	· · · · · · · · · · · · · · · · · · ·	TP7	м = R736 09 С716 мР∨ 08 = Н
- Maria 🛛 🔶 🔶 👘 👘 👘		//</td <td>R622 + C611</td> <td></td> <td>₩ • R728 ₩ • R730</td>	R622 + C611		₩ • R728 ₩ • R730
	J629			R726 C710	-0712
	J628	R6II	TR608		🐅 👔 L E 1936
The second	J627	TR604_		1615	
	J626 J625	R628 C608		J614	
0	Commence -	C609 • +		R618 TR703	
and a second	J624 J623		TR607		
HB	J622	TR603 W R607 J		R616	
HB manufactor	J621	and the summer sector and the sector of the		R617	709 W-970
	J620	Contraction of the second seco			
MV		A Commencement and a second se			
and the second se	J619	1 8 8 8 P - 1 - 10000000			
MV MŞ	J618	and the second s	1608 R614	전 같은 이 같은 것 같아요. 같아요.	AAAAAA
MV MŞ KŞ C706 E -11	JEIB L703		1607	P773	02 m
MV MŞ KŞ E € C706 -IF€ •IF€	707 R 609 CC07	• • • • • • • • • • • • • • • • • • •	1606	R773	02 m
MV MŞ E E -II-€ -II-€ C706- -II-€ C706- -II-€ C706- -II-€ C706- -II-€ C706- -II-€ C706- -II-€ C706- C70- C706- C7	707 R 609 CC07	→ → p602 → → D601 → ₩→ R506	LIN SIN .	R773	02 01 526 701 8704
	707 R 609 C 607 1617 R717 R 605 C 607 1617 R 717 R 605 C 607	→ → → → → → → → → → → → → → → → → → →	1606 1602	R7773 + R7 + N+ C736 C77	02 01 526 R704 R703
MV MS C706 C70	J618           07         R609         C607           J617         R717         •N           W         M         R605         C607           W         M         C606         C607           W         M         C607         C607           W         M         C604         C604           W         M         C604         C604           W         M         C604         C604	→ → → → → → → → → → → → → → → → → → →	1606 1602 1604 1603	R773	02 01 02 02 01 01 02 01 01 02 01 02 02 02 02 02 02 02 02 02 02
MV MS E -11+ R718 + R718 + R718 + R712 R712	<u>J618</u> 1707 R609 C607 C607 R605 C607 R605 C607 R605 C604 R605 C604 R605 C607 R605 C607 R605 C607 R605 C607 R605 C607 R605 C607 C607 R605 C607 C60	→ p602 → p601 → p601 → p601 → p600 → p60	<u>J606</u> J602 J604	R773 + R C736 C776 JG01	02 01 02 02 01 02 01 01 02 01 02 02 02 02 02 02 02 02 02 02
MV MS E C706 -II- -II- C706  C706 	J618         1703           707         R609         C607           J617         R717         R14           W         R005         C604           W         R14         C708           KH         C708         R603           C601         C602         C602	→ p602 → p601 → p601 → p601 → p600 → p60		R773 + 4 C736 C736 C77 5 C77 F R C736 C77 F F R	02 01 02 02 01 01 02 01 01 02 01 02 02 02 02 02 02 02 02 02 02

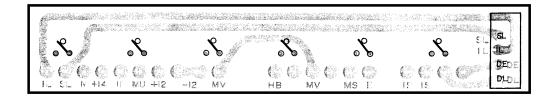
# CONTROL CIRCUIT BOARD NA06484

E.

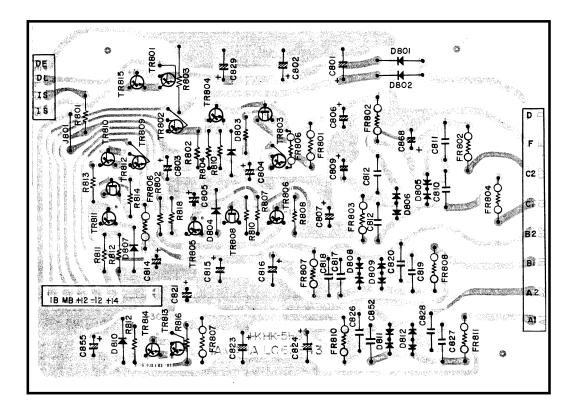
# CT-7000

## SWITCH CIRCUIT BOARD NA06487





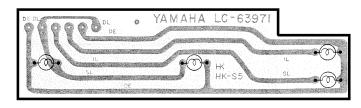
## POWER CIRCUIT BOARD NA06485



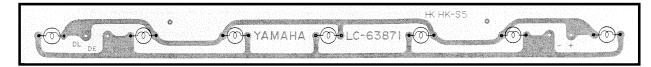
#### MUTING OUTPUT HEADPHONE LEVEL LEVEL V R903 2 ۲ -A VR901 VR902 IOKO 11 \_C63862 AIOKAX2 A10KΩ \*2 YAMAH ¢ 190 CL NL NR OR MR MV-I2 **W** 7 TY

**VOLUME CIRCUIT BOARD NA06488** 

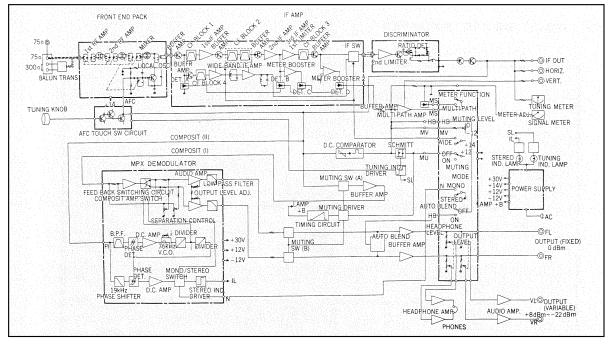
## METER LAMP CIRCUIT BOARD NA06490



## LAMP CIRCUIT BOARD NA06489

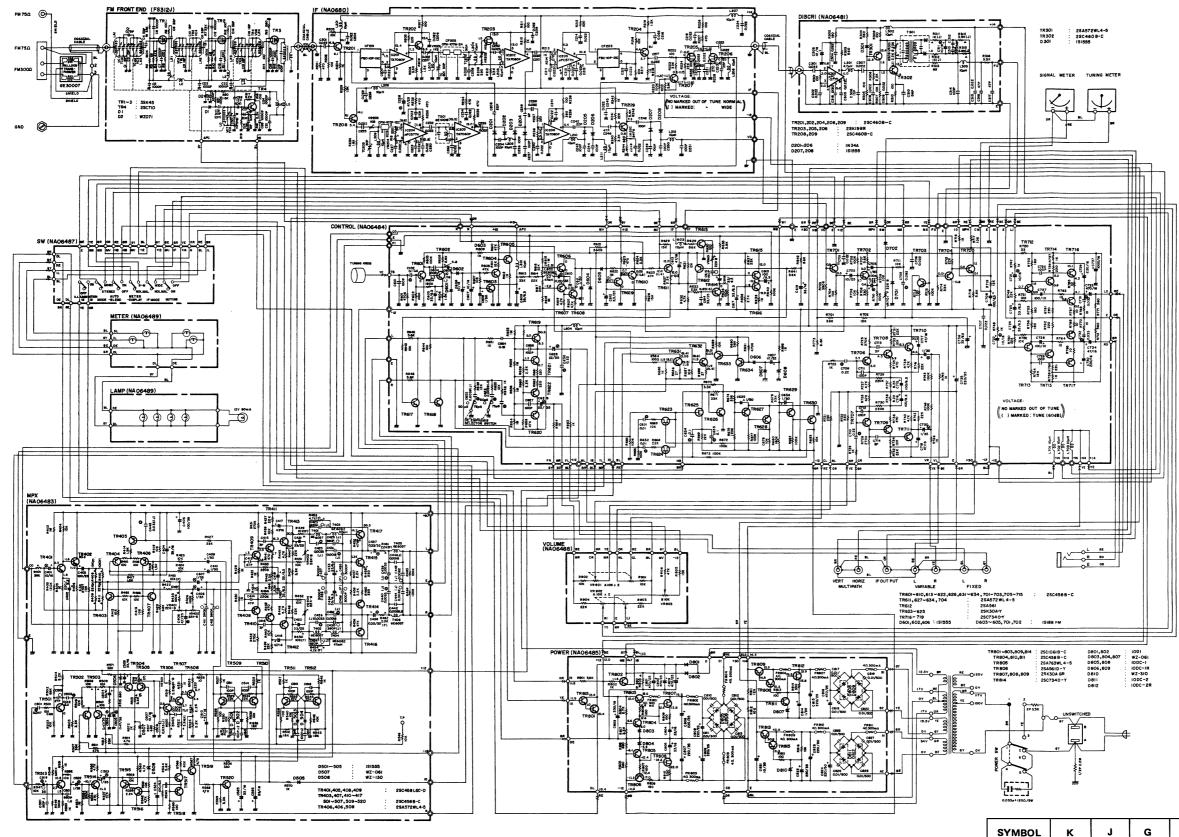


# BLOCK DIAGRAM



<ul> <li>RESISTOR</li> </ul>		<ul> <li>CAPACIT</li> </ul>	••••		• 11112 0020	R ABBREVIATION
SYMBOL	PART NAME	SYMBOL	PART NAME	REMARKS		
o	FUSE RESISTOR	0	MYLAR CAPACITOR		BL 🕨 Black	VI 🕨 Violet
Δ	METALIZED OXIDATION RESISTOR	NO MARK	CERAMIC CAPACITOR		BR 🕨 Brown	GY 🕨 Gray
0	CEMENT RESISTOR	0	POLYSTYRENE CAPACITOR	1	RE 🕨 Red	WH > White
NO MARK	CARBON RESISTOR		(BI-POLAR) ELECTROLYTIC		OR > Orange	GG ► Light Gree
8	CEMENT MOLDED RESISTOR		CAPACITOR		YE 🕨 Yellow	SB 🕨 Light Blue
⊠ ▲	METALIZED FILM RESISTOR	۲	LOW-NOISE ELECTROLYTIC CAPACITOR		GR ► Green	PK 🕨 Pink
		8	TANTALUM CAPACITOR	1	BE 🕨 Blue	

## **OVERALL SCHEMATIC DIAGRAM**



27

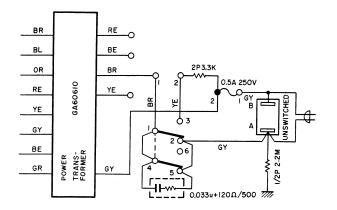
к	J	G	F	D	в
±10	±5	±2	±1.0	±0.5	±0.1

ERROR (%)

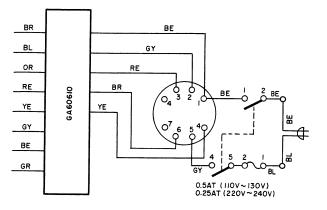
# PARTIAL CHANGES MADE ACCORDING TO DESTINATION

## **v** POWER

• U.S. & CANADIAN MODELS

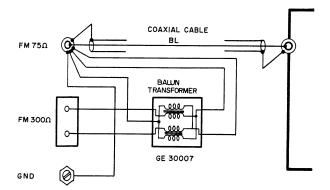


• EUROPEAN MODEL



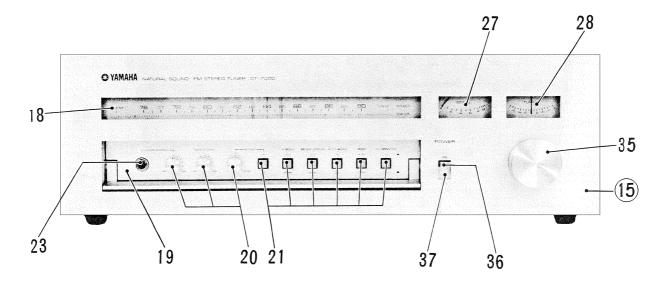
## ▼ ANTENNA

## • EUROPEAN MODEL

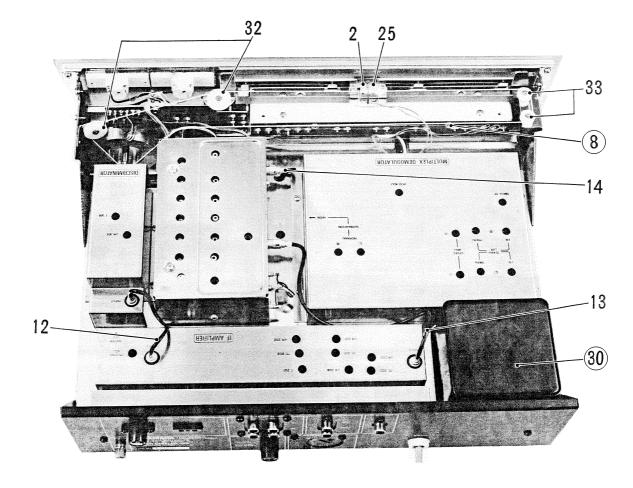


# PARTS LIST

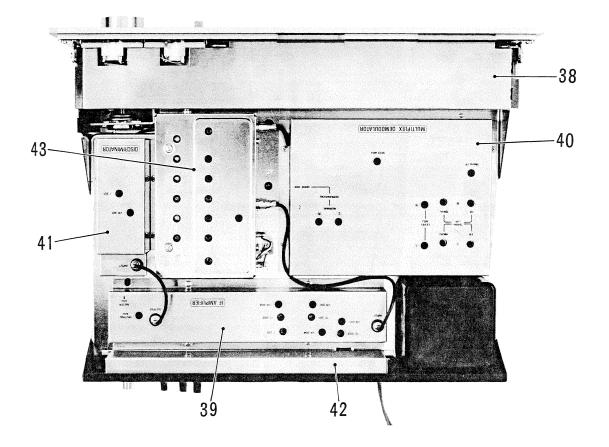
## FRONT VIEW

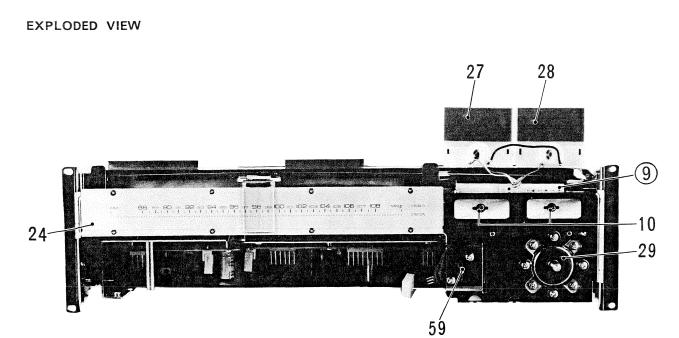


INTERNAL VIEW

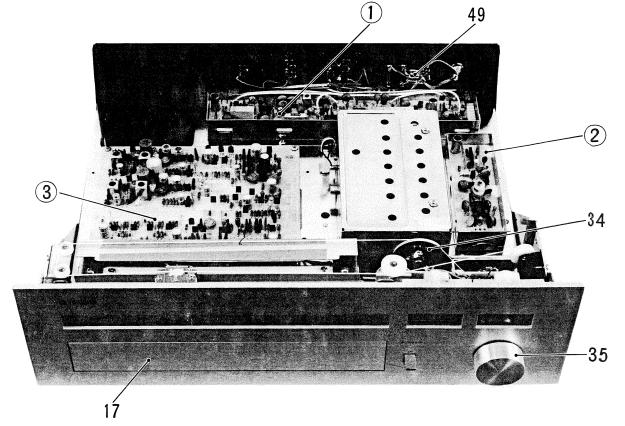


INTERNAL VIEW

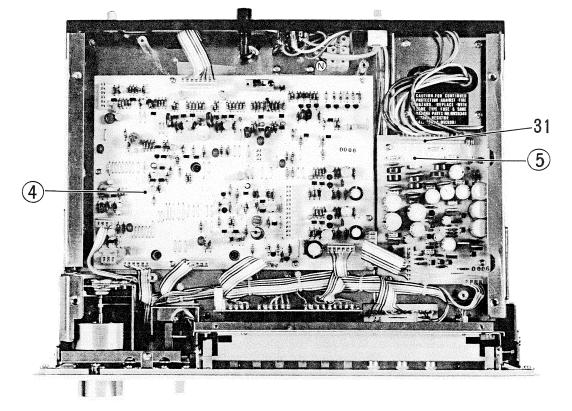


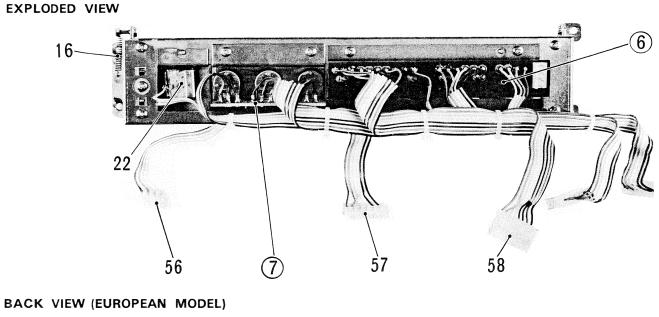


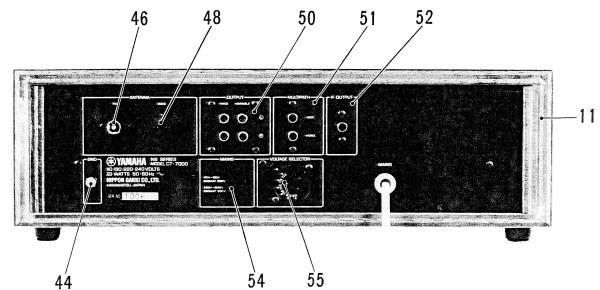
# INTERNAL VIEW



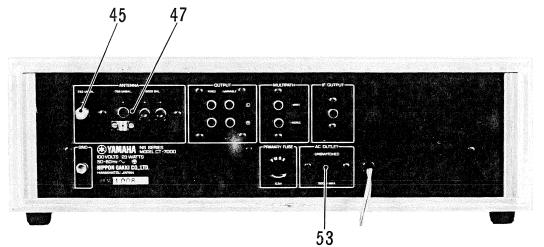
INTERNAL VIEW (BOTTOM)







BACK VIEW



# CT-7000

Ref. No.	Part No.		Descriptions		Remarks	Common Models	
1	NA06480	IF Circuit board	<b>#62754</b>	IFシート			
	FP14647	Tantalum capacitor	4.7 #F 25V	タンタルコンデンサ			
	GE30013	RF inductor	10 <i>µ</i> H	RFインダクター			
	GG00013	Ceramic filter	FBC-10P-01C	セラミックフィルター			
	GG00014	"	CFM107M-24	"			
	GG00015	"	CFM107P-12C	"			
	FY00004	Ceramic trimmer	30PF	セラミックトリマー			
	1 100004						
	HY00019	Metal glaze variable resisto	r CR19R R-1K	メタルグレーズV R			
	HT41001	Variable resistor	SV10KR B220Ω	ソリッドV R			
	1141001	4 01 10DIC   C313(UI	SVIUKI DZZU	2 9 9 P V R			
	iC04588	Transistor	2SC458 B or C	トランジスター			
	iC04588	Transistor	2SC460 B or C	× 7 2 2 2 X 3 -			
	iE00009	FET	2SK19 GR	F E T			
		<b>D</b> . <i>1</i>	10100 514	<i>H A</i> + ···			
	iF00033	Diode	1S188 FM1	ダイオード			
	iF00004	"	1\$1555	"			
	iG00040	IC	TA7060P				
	iG00039	IC	μPC577H	1 C			
	GE10018	FM IFT	GE10018	FM IFT			
	BB06336	IF shield frame	No. 6336	IFシールド枠			
	LB10009	Pin jack SQ3055		ピンジャック			
	LB60052	Connector housing	No. 2145-6B	コネクトコン ハウジング			
2	NA06481	Discriminator circuit board	#62763	ディスクリシート			
	HU45610	Metal film resistor (J)	1ΚΩ	金属被膜抵抗(J)			
	HU45622	"	2.2Κ Ω	"			
	HU45639	"	3.9ΚΩ	"			
	FP13710	Tantalum capacitor	10 #F 16V	タンタルコン			
	GE30014	RF inductor	4.7 <i>µ</i> H	RFインダクター			
	GE30013	"	10 µH	"			

Ref. No.	Part No.	Description	Remarks	Common Models		
	iA07630	Transistor 2SA763 WL 4 or 5	トフンジスター			
	iC04608	" 2SC460 B or C	"			
	iG00039	IC μPC 577H	1 C			
	iF00004	Diode 1S1555	ダイオード			
	HY00016	Metal glaze variable resistor CR19R B-22K	メタルグレーズVR			
	GE10016	Discriminator coil (White marked)	ディスクリコイル	Japanese model		
	GE10017	" (Red marked)	"	U.S. and European models		
	LB10009	1P pin jack SQ3055	1 P ピンジャック			
	LB30022	Connector 2145-38	コネクトコン コネクター			
	BB06337	Discriminator shield frame No. 6337	ディスクリシールド枠			
3	NA06483	MPX circuit board #63473	MPXシート			
	HU47620	Metal film resistor (F: $\pm$ 1%) 2K $\Omega$	金属被膜抵抗			
	HU47633	" " 3.3K Ω	"			
	HU47622	" " 2.2ΚΩ	"			
	HU47647	" " 4.7Κ Ω	"			
	HU47656	" " 5.6K Ω	"			
	HU47710	" " 10K Ω	"			
	HU47722	" " 22KΩ	"			
	HU45610	" (J: ±5%) 1KΩ	"			
	HU45633	" " 3.3KΩ	"			
	HU45710	" " 10K Ω	"			
	HU45739	" " <b>39K</b> Ω	"			
	HU45810	" " 100K Ω	"			
	FD19330	Polystyrene capacitor (F: ±1%) 3000PF	スチコンタテ型			
	FG10030	Ceramic capacitor 50VSL (F: ±1%) 3PF	セラコン			
	FP12733	Tantalum capacitor 33 µ F 10V	タンタルコン			
	FP13710	" 10 # F 16V	"			
	FP14647	" 4.7 µF 25V	"			
	FP15533	" 0.33 # F 35V	"			
	FP15547	" 0.47 µF 35V	"			
	FP15610	" 1 #F 35V	"			

Ref. No.	Part No.		Description		Remarks	Common Models
	HY00019	Metal glaze variable re	sistor CR19R B-1KΩ	メタルグレーズVR		
	HY00016	11	Β-22ΚΩ	"		
	GE20008	MPX coil		мрхコイル	=GE6057	
	GE20011	MPX fixed coil	47mH	MPX固定コイル	=GE6062	
	iA07630	Transistor	2SA763 WL4 or 5	トランジスター		
	iC04588	,,	2SC458 B or C	"		
	iC04589	"	2SC458LG C or D	"		
	iF00004	Diode	1\$1555	ダイオード		
	iF00027	Zener diode	WZ-061	ツェナダイオード		
	iF00035	н	WZ-130	"		
	LB60052	Connector housing	6P 2145-6B	コネクトコン ハウジング		
4	NA06484	Control circuit board	#63503	コントロールシート		
	HY00016	Metal glaze variable res	istor CR19R B22K	メタルグレーズVR		
	FG10030	Ceramic capacitor	50V SL (F:±1%) 3P	セ ラ コ ン		
	FP13710	Tantalum capacitor	10 #F 16V	タンタルコン		
	FP15522	"	0.22 µF 35V	"		
	FP15610	"	1 #F 35V	"		
	FP15622	"	2.2 #F 35V	"		
	GE20011	MPX fixed coil	47 m H	MPX固定コイル	=GE6062	
	GE30013	RF inductor	10 #H	RFインダクター		
	iA05617	Transistor	2SA561 O or Y	トランジスター		
	iA07630		2SA763 WL4 or 5	"		
	iC04588	"	2SC458 B or C	"		
	iC04608	"	2SC460 B or C	"		
	iC07343	"	2SC734 O or Y	"		
	iE00001	FET	25K30A Y	FET		
	iF00004	Diode	1\$1555	ダイオード		
	iF00033	"	1S188 FM1	"		
	iF00032	Zener diode	WZ-061	ツェナータイオード		

Ref. No.	Part No.		Description		Remarks	Common Models	
	L840013	Connector	2403-4A	コネクトコン 極性付ウェハー			
	L860053	"	2403-6A				
	L860054	"	2403-8A	"			
	L830022	Connector	2145-38	コネクトコン			
	L860052	"	2145-68	"			
5	NA06485	Power supply circuit board	<b>#63843</b>	電 源 シ ー ト	Japanese and U.S. models		
	NA06486	"	"	"	European model		
	HW20340	Fuse resistor	300mA 4Ω	ヒューズ抵抗			
	iA05612	Transistor	2SA561 O or Y	トランジスター			
	iA07630	"	2SA763 WL 4 or 5	"			
	iC04588	"	2SC45B B or C	"			
_	iC07343	"	2SC734 O or Y	"			
	iC10613		2SC1061 8 or C	"			
	iE00002	FET	2SK30A GR	FET			
	iF00032	Zener Diode	WZ-061	ツェナーダイオード			
	iF00022	"	WZ-310	"			
	iH00003	Diode	10D-1	ダイオード			
	iH00008	"	10DC-1	"			
	iH00009	"	10DC-1R	"			
	iH00005	"	10DC-2	"			
	iH00013	"	10DC-2R	"			
				<u> </u>   ネクトコン			
	L840013	Connector	2403-4A	コ ネ ク ト コ ン 極性付ウェハ~			
	L860054	"	2403-8A	"			
	L860055	<i>"</i>	5015-8A	"			
6	NA06487	Switch circuit board ‡	63852	スイッチシート			
	KA90006	Switch	SC8 11058	ス イ ッ チ			
	L840013	Connector	2403-4A	コ ネ ク ト コ ン 極 性 付 ウ ェ ハ ー			
0	NA06488	Volume circuit board	#63862	V R シート			
	1100400		π 03002				

Ref. No.	Part No.		Description		Remarks	Common MOdels	
	HR10007	Variable resistor	RV16YP15S Β-10KΩ	可変抵抗			
	HR10008	"	RV16YPG15S A-10K×2	"			
8	NA06489	Lamp circuit board	#63870	フンプシート			
	JB00031	Lamp 12V 60mA		パイロットランプ			
9	NA06490	Meter lamp circuit board	#63970	メーターランプシート			
10	JB00031	Lamp 12V 60mA		パイロットランプ			
11	32007070050000	Outside case	(AW)	外 装 (ウォールナット)	U.S. and European models		
12	MZ06487	Pin-Pin coaxial cable	<i>l</i> = 150 mm	  両端ピンプラグ付  同軸 ケーブル			
13	MZ06488	"	ℓ = 300 mm	//			
14	MZ06364	Coaxial cable with pin plug	ℓ= 570 mm	片端 ″			
()	NB06963	Panel unit		パネルユニット			
16	AA07637	Hing spring		ヒンジスプリング			
17	BA06534	Rolling panel		パネル蓋			
18	CG06032	Dial glass		ダイアルガラス			
19	BA06537	Sub-panel		サブパネル			
20	BA06541	Variable resistor knob		V R ツマミ			
21	CB07037	Push button		プッシュボタン			
22	LB30007	Headphone jack	JH5020K	ホーンジャック			
23	CB06827	Phone nut		ホーンナット			
24	NB06966	Dial scale unit		ダイアルスケ ル ユ ニ ッ ト	U.S. and European models		
25	NB06967	Dial pointer unit		ダイアルポインタ ユニット			
26	JB00009	Lead type lamp	12V 60mA	リ ド式ランプ			
27	Ji 00026	Signal meter		シグナルメーター			
28	Ji00027	Tunig meter		チューニングメーター			
29	NB06969	Tuning unit		チューニングユニット			
30	MZ06482	Power transformer assembly		電源 トランス アッセンブリ	U.S. model		

Ref. No.	Part No.		Remarks	Common Models			
	MZ06483	Power transformer assem	bly	<b>電源トランス</b> アッセンブリ	European model		
31	LB60059	Connector housing	No. 2139-15	コネクトコン配線用			
51	LB00039	Connector nousing	100, 2139-15	ハウジング			
32	CB07094	Pulley	No. 7094	滑 車			
33	CB07034	11	No. 7034	"			
34	CB06322	Pulley for variable capacitor		バリコンプーリー			
35	BA06540	Tuning knob	No. 6540	チューニングツマミ			
36	CB06857	Lever knob	No. 6557	レバーツマミ		CA-1000	
	CB06858	Bush for switch		S W 用 ブッシュ			
37	CB06872	Switch apron		スイッチェプロン		"	
38	AA07603	Sub cover		サブカバー			
39	AA07618	IF cover		IFカバー			
40	AA07619	MPX cover		MPXカバー		_	
41	AA07620	Discriminator cover		ディスクリカバー			
42	AA07621	Rear cover		リアーカバー			
43	PA00027	RF pack	FS-312U	R F パック	U.S. and European models		
	LB30023	Connector on shassis	No. 2220-3Y	コネクトコン シャーシーマウント			
	LB60056	п	No. 2220-6Y	"			
	LB60057	"	No. 2220-12Y	"			
	LA00104	3P connection terminal		3 P 中継端子台	European model		
	1 4 0 0 0 7 0						
44	LA00079	Ground terminal B	E 61A	ア ー ス 端 子 B F型レセプタクル	Japanese and U.S. models		
45 46	LB20016	F type receptacle 75 $\Omega$ coaxial cable connect					
46	LB20015 LA00134	Antenna terminal 3P	LIUI SUCKET L-OUS	<u>ソケット</u> アンテナ端子	European model Japanese and U.S.		
48	LB20007	DIN FM antenna socket	CS-082		models European model		
40	GE30007	Balun transformer	03-002	<u>ソケット</u> バルーントランス		+ +	
50	LB40017	Pin jack 4P	JPC-214				
51	LB20079	" 2P	JPC-096	"			
52	LB10028	" 1P	JPC-066	"			
53	LB10020	AC socket	503B	 A C ソ ケ ッ ト			
	LB20030	Fuse holder UL type	SMK SN-1301	ヒューズホルダー	U.S. model	+ +	
54	LB20048	"	FEB-031-1401	"	European model		
	КВ00101	UL type fuse SS-2	0.5A 250V	ヒューズ	U.S. model		
		Miniature fuse			European model		

Ref. No.	Part No.		Descr	iption		Remarks	Common Models	
55	LB20025	Voltage selector				European model		
	HL42656	Metal oxid film resistor	5.6K Ω	2W	酸化金属被膜抵抗	Japanese and U.S. models		
	HL42633	,,	3.3K Ω	2W	"	"		
56	LB40014	Connector housing	No. 2139-	4	 コネクトコン配線用 ハウジング			
57	LB60058	"	No. 2139-		ハウジンク			
	LB60040		No. 2139-		"			
58			No. 2139		"		_	
	LB30024	"	140. 2135	5				
			N. 0570	<b>-</b>	コネクトコン			
	LB10024	Contact pin for connector	No. 25/8	•1	ターミナル連鎖状			
						Japanese and U.S.		
59	KA20010	Lever switch (Power switc			レバースイッチ	models	_	
	KA20011	**	JL-0	8	"	European model	_	
	FZ00011	Spark killer	0.033 # F	120 Ω	スパークキラー	Japanese and U.S. models	_	
	MZ06189	Pin-Pin connection cable	No. 6189		接続 コード			
	KB00031	Fuse	0.1AT	250V	ヒューズ	Japanese model		
	KB00101	,,	0.5A	250V	"	U.S. model		
	KB00071	Miniature fuse	0.5AT	250V	S ヒュ - ズ	European model		
	KB00064	\$1	0.25AT	250V	"	"		
	NB06678	Service pad	No. 6678		サービスパッド	U.S. and European models		
	MZ06440	FM Q match antenna	5059-01		FMQマッチアンテナ			
	MZ06459	"	6459		"	European model		
							_	
							_	
							_	
							_	
							_	

